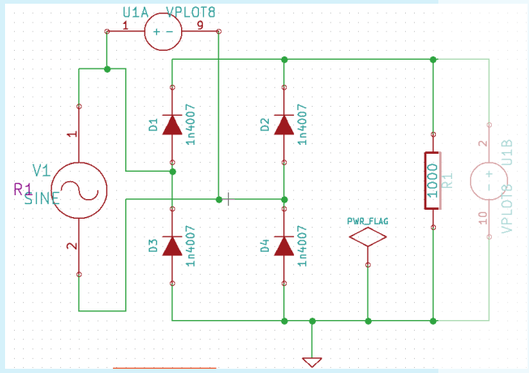
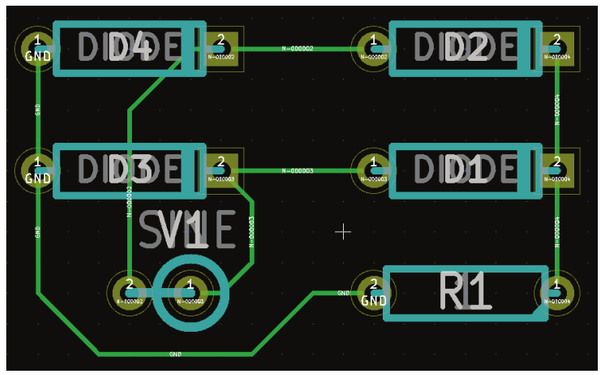
Features

Create Circuit Schematic

- Generate netlists for simulation and PCB design
- Perform Electric Rules Check (ERC)
- Create new components using Library Editor



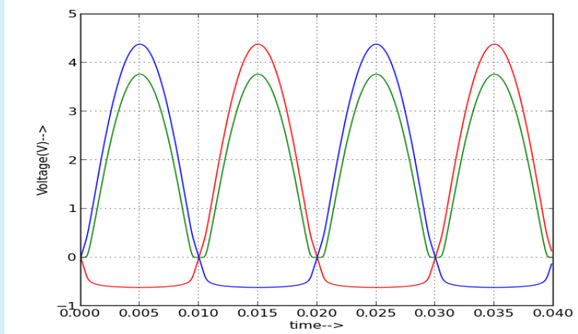


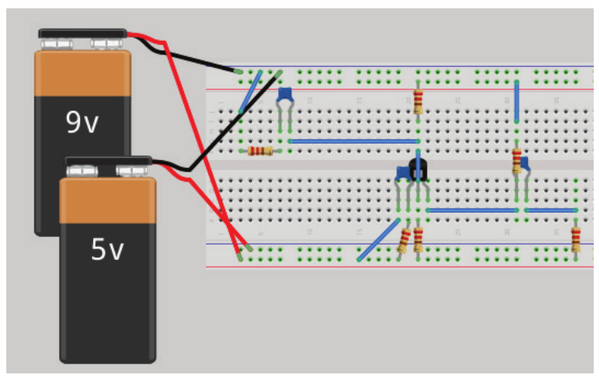
Create PCB Layout

- Lay tracks
- Modify the width of tracks
- Create multi-layer PCB designs
- Add/edit design rules
- Generate Gerber files

Perform Circuit Simulation

- Analog, digital and mixed signal circuit simulations
- Perform AC, DC, nested DC,
- Transient, Fourier and Pole-Zero analyses





Breadboard Based Circuit Design and Simulation

- Design circuits using virtual breadboard, and perform simulations
- Automatically generate circuit
- schematic and PCB layout for documentation

Advanced Features

Model Builder

- Create a new model for an electronic device
- Edit an existing model of an electronic device

PSpice® to FreeEDA Converter

- Convert PSpice schematic to FreeEDA schematic
- Helpful in Lab Migration

Subcircuit Builder

- Create a new subcircuit
- Edit an existing subcircuit

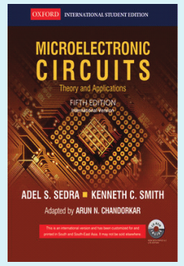
Textbook Companion

FreeEDA textbook companion is one of the major activities supported by the FreeEDA team at FOSSEE, IIT Bombay. This work involves generating FreeEDA project files for all the solved simulation exercises present in standard Electrical and Electronics Engineering textbooks. Participate and earn attractive honorarium and Certificate of Internship from IIT Bombay.

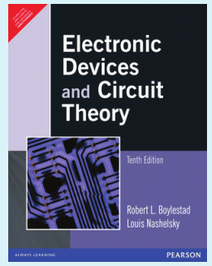
Other advantages include:

- Download and use the simulations
- Available free of cost

Available Textbook Companions:



Microelectronic Circuits: Theory and Applications
(5th Edition)
by Adel S. Sedra, Kenneth C. Smith
Publisher:
Oxford University Press



Electronic Devices And Circuit Theory
(10th Edition)
by Louis Nashelsky, Robert L. Boylestad
Publisher:
Pearson

Lab Migration

Lab Migration is another major activity supported by the FreeEDA team at FOSSEE, IIT Bombay. It aims to migrate labs that use proprietary software (e.g. OrCAD, Pspice) to a Free and Open Source Software (FOSS) only, lab. The Lab Migration team, apart from providing suggestions and solutions to the problem statements, also coordinates the migration of the lab to FOSS - only laboratory. Participate and earn attractive honorarium for your efforts.

Other advantages include:

- Save money and avoid software piracy by shifting to FOSS - only lab
- Get free training from FreeEDA team through free workshops for faculty, students, and lab staff
- Labs that migrate first will receive a special mention on our website

How to participate?

Textbook Companion

- Create FreeEDA textbook companions for any standard books on electronic circuits
- Use FreeEDA Textbook Companion and give feedback

To know more, please visit:
http://freeeda.in/textbook-companion-internship

Lab Migration

- Migrate Electronic/Electrical circuits labs in your college, from proprietary CAD software to FreeEDA

To know more, please visit:
http://freeeda.in/procedure-lab-migration