

Lab Migration

This activity aims to migrate labs that use proprietary software to eSim. eSim offers the same capabilities for circuit design, simulation, analysis and PCB design as any other proprietary EDA tool. Hence educational institutions can save a huge amount of money which they may spend on procuring licenses.

The Lab Migration team helps in the following ways:

- Provide suggestions on the different ways eSim can be implemented in the laboratory
- Coordinate the migration of the lab to eSim
- Provide eSim solutions to the lab's problem statements
- Conduct eSim workshops free of cost.

How can one contribute to LM:

- Migrate labs in your institute which are using proprietary EDA tools to eSim
- Provide an undertaking that the migrated labs will use eSim for the next two academic sessions
- Earn attractive honorarium and a Certificate of Participation from FOSSEE, IIT Bombay

Labs migrated to eSim:

- Open Source EDA Lab
 - Electronics Engineering, National Institute of Science and Technology

Following are the lab problems for which eSim solutions are available:

- Full Wave Rectifier
- Clipper Circuit using Diode
- BJT Biasing Circuit
- BJT Amplifier Circuit
- Frequency Response of BJT
- Subtractor using OPAMP
- OPAMP Inverting Amplifier
- Digital Logic Gate Testing
- Half Adder
- Full Adder
- SOP Implementation
- Study of Multiplexer
- Full Subtractor
- Binary to Gray Converter
- PISO Register

Labs Under Progress:

- Integrated Circuits Lab
 - Instrumentation Engineering, Dr.Mahalingam College of Engg & Tech. Pollachi
- Design Lab
 - Electronics Engineering, Haldia Institute of Technology

* esim.fossee.in/lab-migration-project

Self Workshop

The eSim team conducts workshops for faculty development to promote eSim in education. These workshops demonstrate how eSim can be used for simulating electronic circuits. For self learning, we recommend you to use the Spoken Tutorials available on our web site.

Forum

Forum is a place where one can post all their doubts and questions which users / developers get while using eSim. For this purpose, the eSim team, FOSSEE, IIT Bombay operates it's own forum.

<http://forums.fossee.in>

About FOSSEE

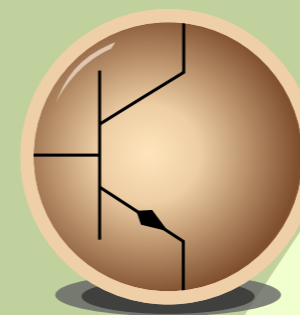
FOSSEE (Free and Open Source Software for Education) project is funded by the National Mission on Education through ICT, MHRD. The FOSSEE team works on 'Adaptation and development of Open Source simulation packages equivalent to proprietary software', and is based at Indian Institute of Technology Bombay.

Other Projects under FOSSEE

Scilab, Python, DWSIM, Osdag, Sandhi, FOSSEE Laptop, OpenFOAM, OR Tools, OpenModelica

Activities of FOSSEE

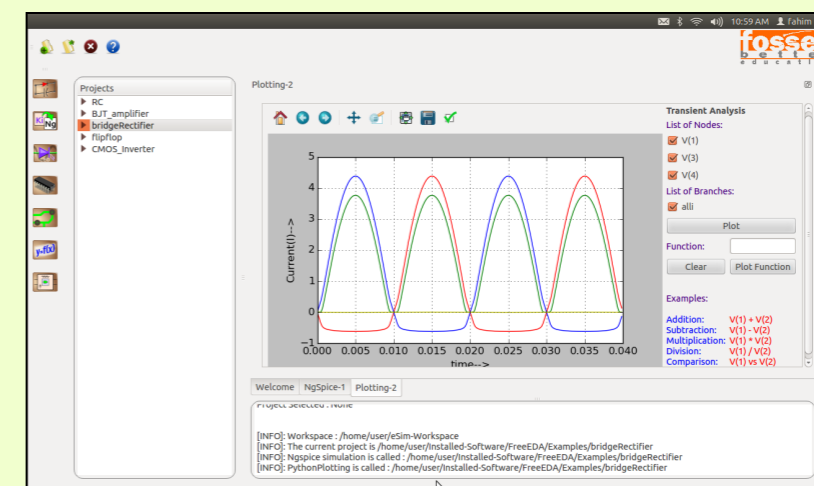
- Textbook Companion
- Low Cost Laptop
- Lab Migration
- Forums
- Spoken Tutorials



eSim

<http://esim.fossee.in>

A Free and Open Source EDA Tool



eSim front end

eSim (formerly known as Osdag/FreeEDA) is an open source EDA tool for circuit design, simulation, analysis and PCB design, developed by FOSSEE team at IIT Bombay. It is released under GNU GPL License & runs on Ubuntu Linux, Windows XP and Windows 7.

It is an integrated tool built using open source software, such as, KiCad (<http://www.kicad-pcb.org>) and Ngspice (<http://ngspice.sourceforge.net>). eSim has been successfully ported to low cost FOSSEE Laptop (<http://laptop.fossee.in>).



General help, Queries & Workshop requests:
contact-esim@fossee.in

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<http://sakshat.ac.in>